

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: OGINO et al.
Serial No.: Not yet assigned
Filed: On even date
For: SEMICONDUCTOR DEVICE, SEMICONDUCTOR WAFER,
SEMICONDUCTOR MODULE, AND A METHOD OF
MANUFACTURING SEMICONDUCTOR DEVICE

JC675 U.S. PTO
09/429297
10/28/99

INFORMATION DISCLOSURE STATEMENT
UNDER 37 CFR 1.97 & 1.98

Assistant Commissioner for Patents
Washington, D.C. 20231

October 28, 1999

Sir:

In the matter of the above-identified application, applicants are submitting herewith copies of the documents listed in the attached form equivalent to Form PTO-1449 for the Examiner's consideration.

This information disclosure statement is being submitted with the new application.

To the extent the documents listed on the attached form equivalent to Form PTO-1449 are not in the English language, the requirement of 37 CFR 1.98(a)(3) for a concise explanation of relevance is satisfied by the discussion of these documents in the specification.

It is respectfully requested that this information disclosure statement be considered by the Examiner.

Please charge any shortage in the fees due in connection with the filing of this paper, including extension of time fees, to the deposit account of Antonelli, Terry, Stout &

#5
6-18-00
Payton

Kraus Deposit Account No. 01-2135 (Case: 503.37770X00), and
please credit any excess fees to such deposit account.

Respectfully submitted,

ANTONELLI, TERRY, STOUT & KRAUS LLP



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Attachments

